

3. Hybrid - this model is the newest one and it combines both accusatorial and inquisitorial: there are two parties, court is passive and its only function is justice. Parties compete to persuade the court to pass the needed verdict. Court compares evidence and arguments gathered by parties and passes a verdict. This model is used in modern Ukraine because it is by far the most fair model.

To put it in a nutshell, models of criminal justice are not just theoretical constructions, they are schemes, which display historical development and current state of judicial system.

References:

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INTELLECTUAL PROPERTY RIGHT TO LAYOUT DESIGNS (TOPOGRAPHIES) OF INTEGRATED CIRCUITS

Layout Designs (topographies) of Integrated Circuits are a field of the protection of intellectual property. Integrated Circuit shall mean a finished or half-finished product that contains various elements, at least one of which is active, that are partly or entirely interconnected and integratedly formed in a semiconductor to produce electronic functions. It is a certain combination of chips that provide the achievement of a technical result. A processor, flash memory are examples of layout designs of integrated circuits. Almost all household appliances contain Layout Design of Integrated Circuit – TVs, laptops, washing machines etc.

The Civil Code, Law of Ukraine «On the Protection of Rights to the Layout Designs of Integrated Circuits» are the main legal acts that regulate intellectual property relations on Layout Designs of Integrated Circuits. Subjects of Intellectual Property Right to Layout designs (topographies) of integrated circuits shall be: 1) the author of Layout designs (topographies) of integrated circuits; 2) other persons that have acquired the rights for Layout designs (topographies) of integrated circuits pursuant to the agreement or law. If the

author includes several persons jointly, the right to Layout Design of Integrated Circuit shall be granted to them jointly, unless agreed otherwise [2].

Right to Layout Design of Integrated Circuit shall be granted to a Layout Design of Integrated Circuits that is original. Layout designs of integrated circuits shall be deemed original if such design is the independent creation of the designer, and while such layout design of integrated circuits was created, it was not a common thing for designers [1].

The Right to Layout Design of Integrated Circuit shall be granted on the basis of application. Acquisition of intellectual property right to Layout designs (topographies) of integrated circuits shall be witnessed by the certificate. Proprietary intellectual property rights to electronic chip assembly which arise from the registration of layout designs (topographies) of integrated circuits shall be: 1) the right to use electronic chip assembly; 2) exclusive right to permit the use of the electronic chip assembly; 3) exclusive right to prevent unlawful use of electronic chip assembly, including prohibition of such use; 4) other proprietary intellectual property rights established by the law. Proprietary intellectual property rights to Layout Designs (topographies) of Integrated Circuits shall belong to the holder of the respective certificate, unless otherwise established by the agreement or the law [2].

Validity of exclusive proprietary intellectual property rights to the electronic chip assembly shall be terminated in 10 years commencing from the date of application for the electronic chip assembly per the procedure established by the law.

Validity of proprietary intellectual property rights to electronic chip assembly can be early terminated upon the initiative of the right holder, unless this contradicts the conditions of the agreement, as well as in other cases stipulated by the law. In case of validity termination of the exclusive proprietary intellectual property rights to electronic chip assembly, it can be freely used by any person, with the exceptions established by the law [2].

To sum up, Layout Designs (topographies) of Integrated Circuits is one of the objects of intellectual property with its special subjects, procedure of registration, proprietary rights. All legal issues regarding this object are regulated by Ukrainian legislation. As a result, subjects of intellectual property rights obtain guarantees of protection by the law.

References:

1. Цивільний Кодекс України від 16.01.2003 № 435-IV (Редакція станом на 07.03.2018)

2. Закон України «Про охорону прав на топографії інтегральних мікросхем». (Відомості Верховної Ради України (ВВР), 1998, N 8, ст. 28).
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LEGAL LIABILITY FOR WATER USE VIOLATION

Ukrainian legislation clearly regulates the issue of the liability for violation of water legislation. It may cause administrative, disciplinary or criminal responsibility. There is also a list of acts for which the response comes.

They include:

- unauthorized seizure of water objects;
- violation of the regime of economic activity in water protection zones and on the lands of the water fund;
- pollution and littering of water objects;
- non-compliance with the conditions of permission or violation of the rules of special water use;
- unauthorized conducting of hydrotechnical works (construction of ponds, dams, canals, wells);
- the use of land for a water fund without intention;
- failure to report (conceal) information on emergency situations on water objects;
- and others.

The first type of responsibility for violation of water legislation is the **disciplinary responsibility**. Disciplinary responsibility arises for violations that may be regarded as violations of labour discipline. It consists of imposing on the offender disciplinary penalties by the administration at the place of work or a higher authority. Such liability arises on the general principles stipulated by the Labour Code of Ukraine. The application of disciplinary liability is provided due to the provisions of Chapter 10 "Labour discipline" of the Labour Code of Ukraine.